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OCT 28 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Application of: )  
Applicants : SHIH-HSIUNG LIN ET AL. ) Examiner: WILLIAMS, ALEXANDER O  
Serial No.: 10/695,630 ) Art Unit: 2826  
Filed: 10/27/2003 ) Docket No.: MEGP0005USA4  
For: METHOD OF ASSEMBLING CHIPS )

**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The Office Action mailed September 29, 2005 has been carefully considered. In response thereto, please enter the following amendments and consider the following remarks.